



#13 D. EVANS
12-26-02

Applicant(s)	Frankie F. Roohparvar	AMENDMENT & RESPONSE TO NON- FINAL OFFICE ACTION
Serial No.	09/608,580	
Filing Date	June 30, 2000	
Group Art Unit	2818	
Examiner Name	Trong Q. Phan	
Confirmation No.	9345	
Attorney Docket No.	400.006US01	
Title: ZERO LATENCY-ZERO BUS TURNAROUND SYNCHRONOUS FLASH MEMORY		

Box Non-Fee Amendment
Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 800

In response to the Non-Final Office Action mailed September 20, 2002, please consider the following remarks.

IN THE SPECIFICATION

Please replace the paragraph starting on page 1, line 28 with the following amended paragraph:

Yet another type of non-volatile memory is a Flash memory. A Flash memory is a type of EEPROM that can be erased and reprogrammed in blocks instead of one byte at a time. Many modern personal computers (PCS) have their basic input/output system (BIOS) stored on a flash memory chip so that it can easily be updated if necessary. Such a BIOS is sometimes called a flash BIOS. Flash memory is also popular in modems because it enables the modem manufacturer to support new protocols as they become standardized.

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